FIG. 1

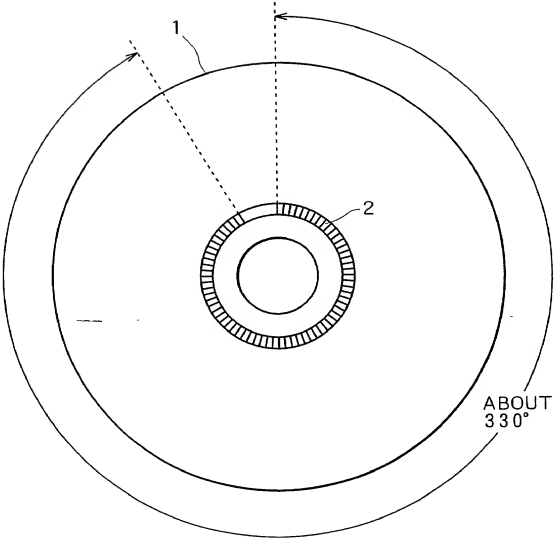
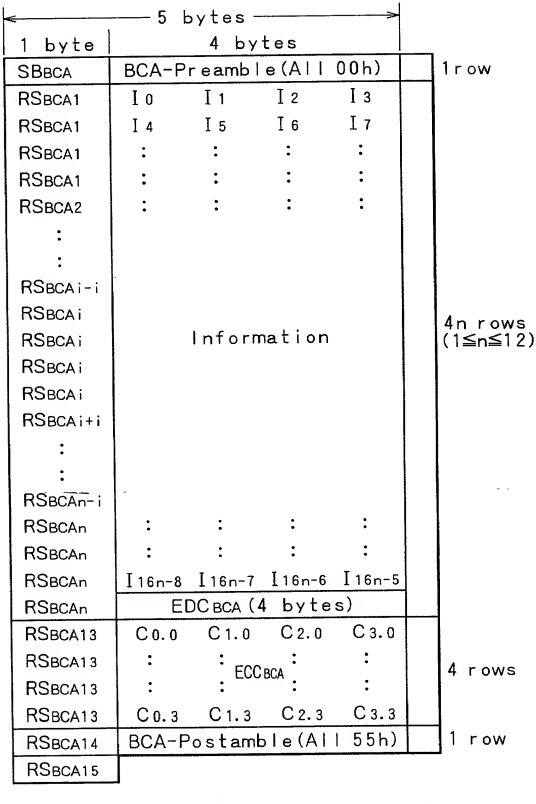


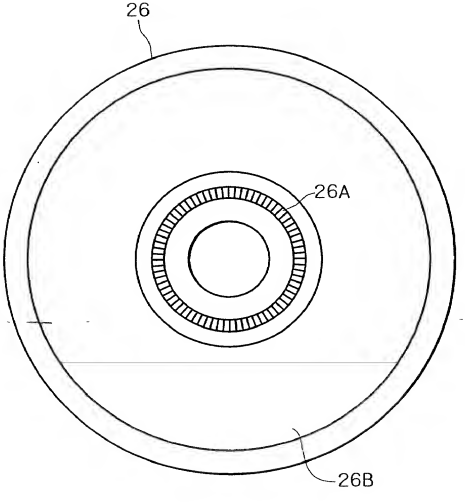
FIG. 2



T00260*96n75660

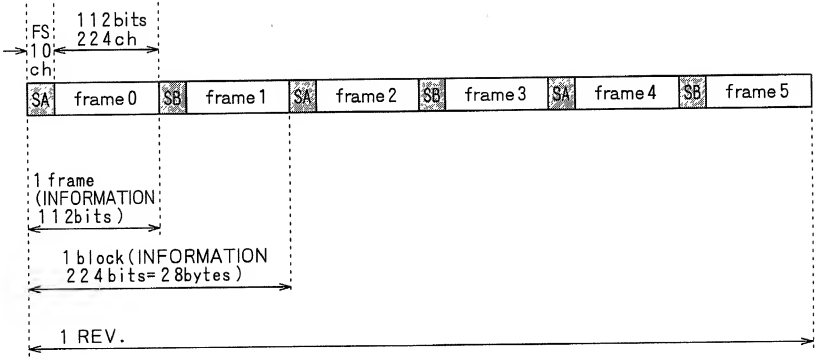
T00260'96h2566O

FIG. 3



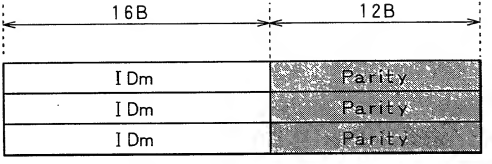
T00260'96h7566D

FIG. 4



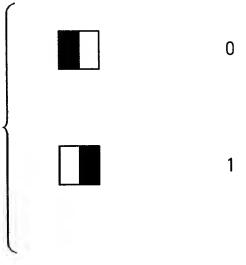
n=3blocks
m=2frames
k=234channel bits =10(Frame Sync)+224(Information)

FIG.5



GF(2^8)
RS(32, 16, 13)×3

FIG.6



F00260 '96h/5660

FIG. 7A

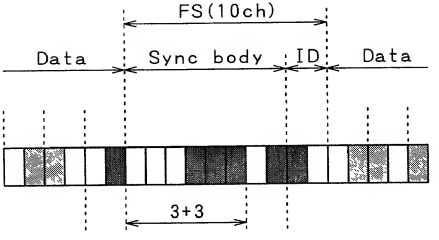
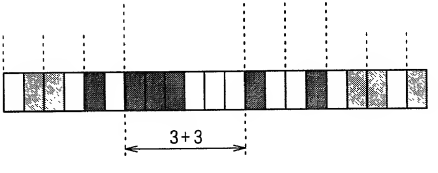
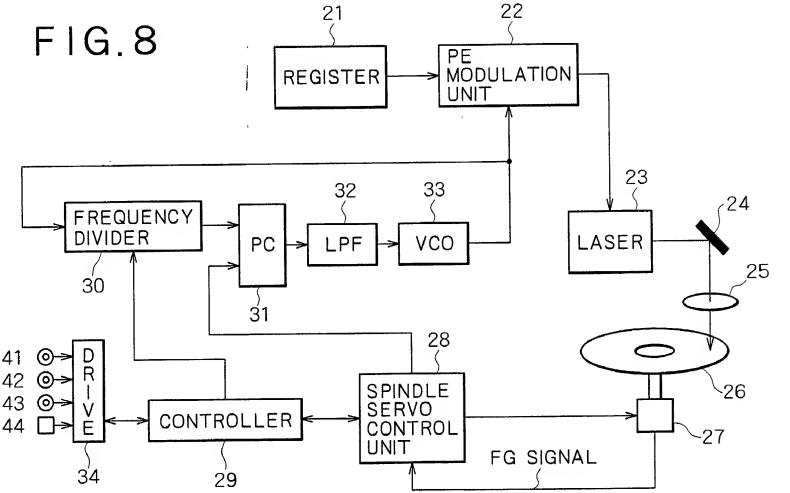


FIG. 7B



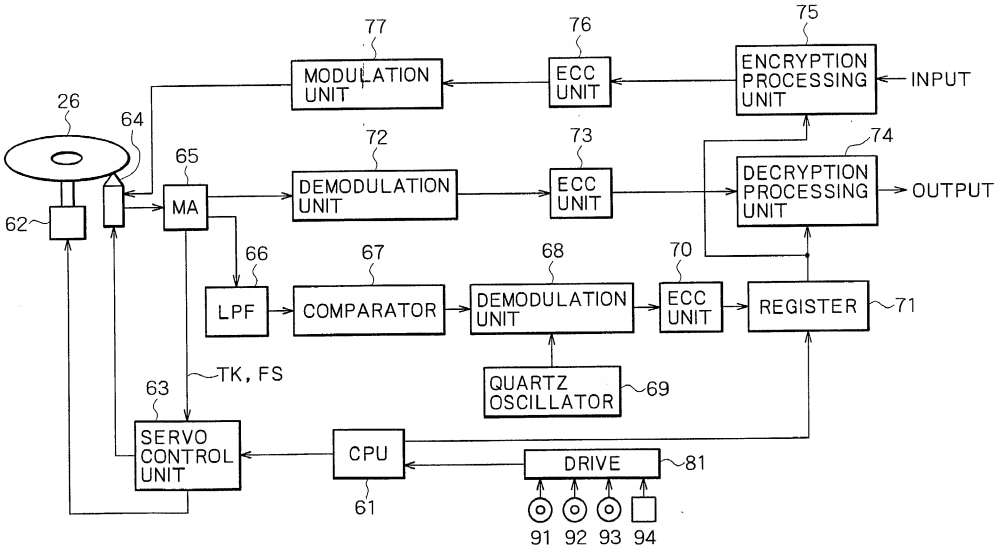
EP0026094A2

FIG. 8



TOO260*96h2S660

FIG. 9



T00260*96h7S660

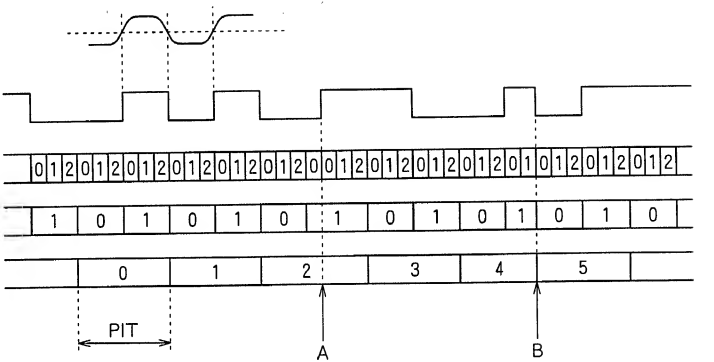
FIG.10A

FIG.10B

FIG.10C

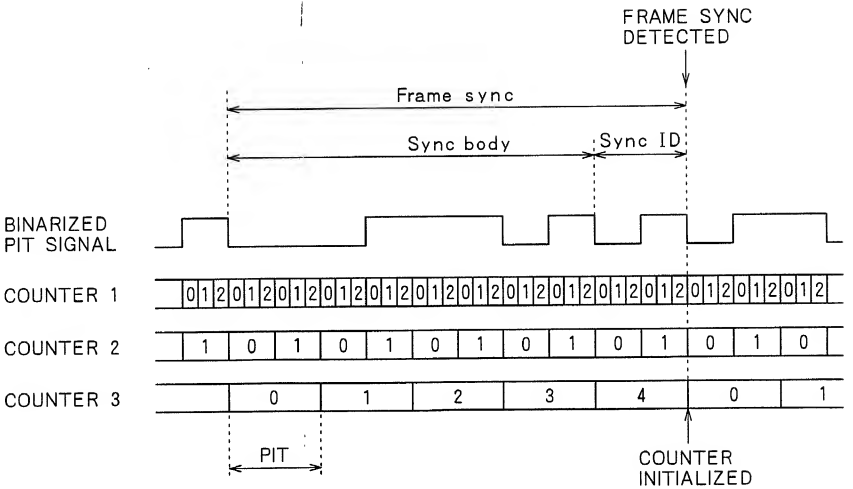
FIG.10D

FIG.10E



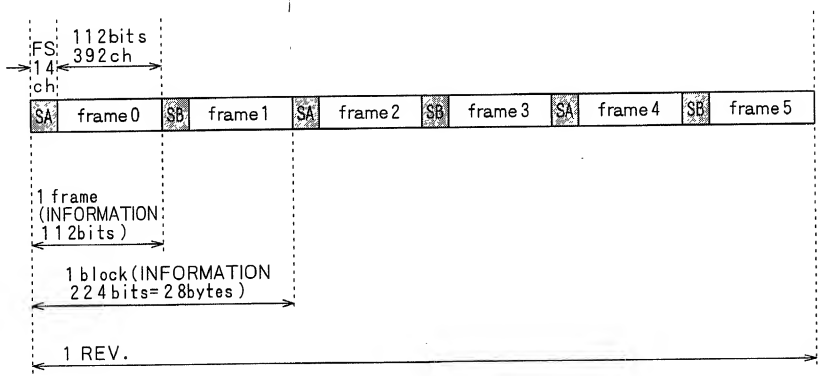
JP 2001-060260 A 2001.3.6

FIG. 11



T00260*96A2S660

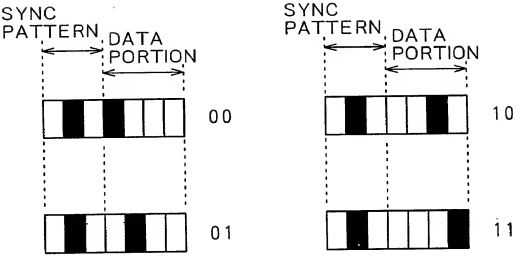
FIG. 12



n=3blocks
m=2frames
k=406channel bits=14(Frame Sync)+392(Information)

T00260*96n7566O

FIG. 13



TD0260'96h75660

FIG. 14A

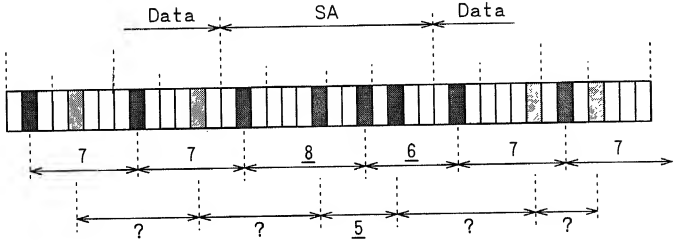


FIG. 14B

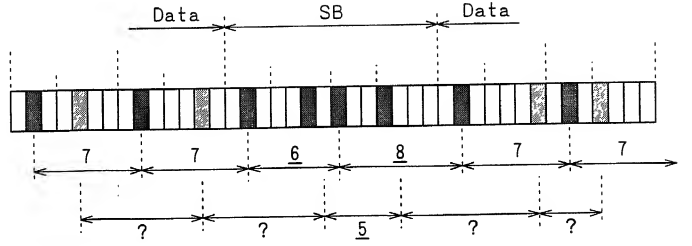
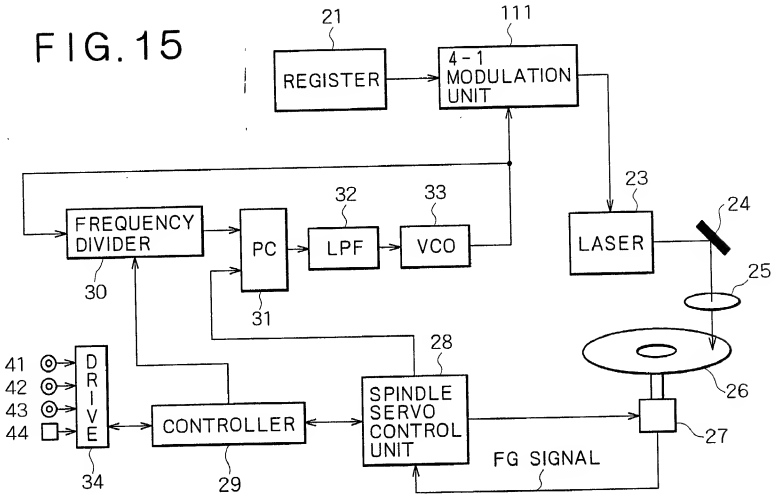


FIG. 15


21
REGISTER
111
4-1
MODULATION
UNIT
23
LASER
24
25
26
27
FREQUENCY
DIVIDER
30
PC
31
32
LPF
33
VCO
28
SPINDLE
SERVO
CONTROL
UNIT
FG SIGNAL
41
42
43
44
DRIVE
34
CONTROLLER
29

TOO260"96h∠S660

FIG. 16A

FIG. 16B

FIG. 16C

FIG. 16D

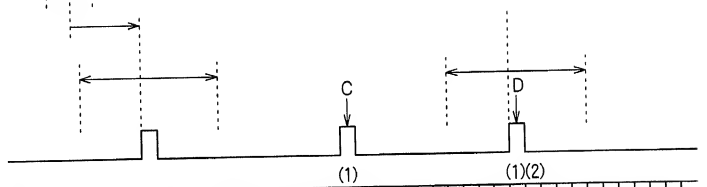


FIG. 16E

FIG. 16F

FIG. 16G

T00260*96h7S660

FIG. 17

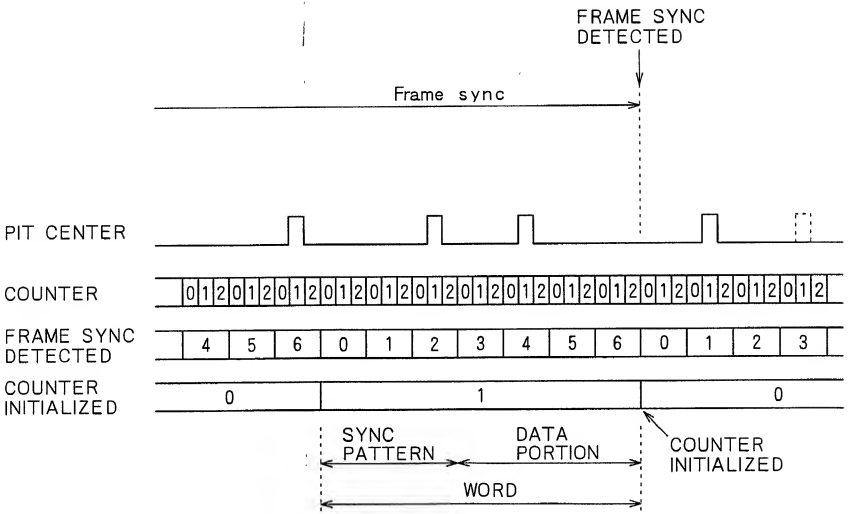


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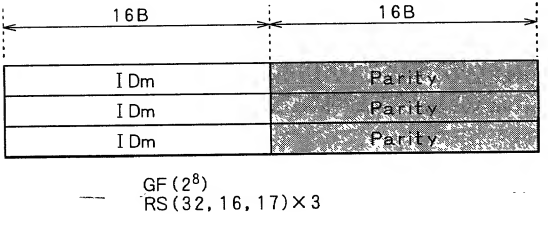
FIG. 18



T00260'96h/S660

FIG. 19A

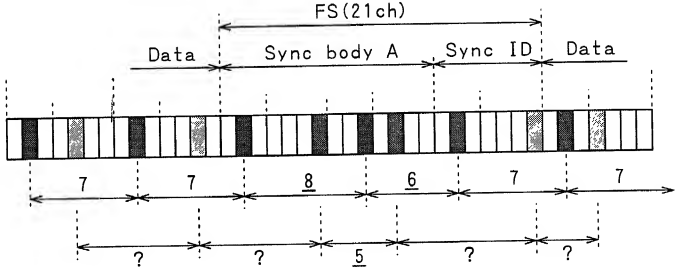


FIG. 19B

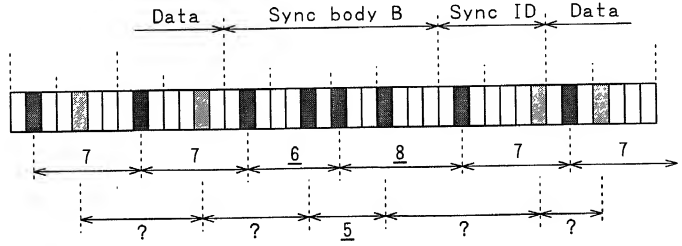
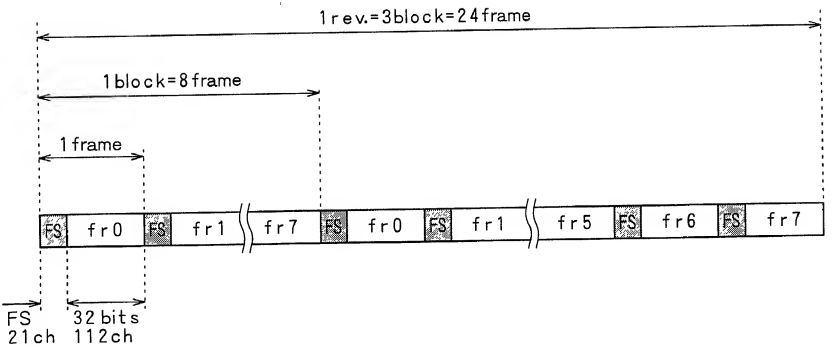


FIG. 20


1 rev.=3block=24frame
1 block=8 frame
1 frame
FS
fr0
FS
fr1
fr7
FS
fr0
FS
fr1
fr5
FS
fr6
FS
fr7
FS
21ch
32 bits
112ch
n=3blocks
m=8frames
k=133channel bits=21(Frame Sync)+112(Information)